

(11) EP 0 772 305 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3: 05.12.2001 Bulletin 2001/49

(51) Int Cl.7: H04B 1/707, H04L 7/033

(43) Date of publication A2: 07.05.1997 Bulletin 1997/19

(21) Application number: 96117432.3

(22) Date of filing: 30.10.1996

(84) Designated Contracting States: **DE FR GB**

(30) Priority: 02.11.1995 JP 31002195

(71) Applicants:

Yozan Inc.
 Tokyo 155-0031 (JP)

 NTT MOBILE COMMUNICATIONS NETWORK INC.
 Minato-ku, Tokyo 105-8436 (JP)

(72) Inventors:

Shou, Guoliang c/o Yozan Inc.
 Setagaya-ku, Tokyo 155, (JP)

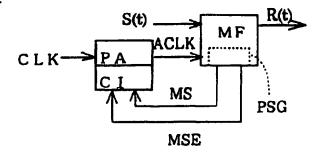
- Zhou, Changming c/o Yozan Inc.
 Setagaya-ku, Tokyo 155, (JP)
- Yamamoto, Makoto c/o Yozan Inc.
 Setagaya-ku, Tokyo 155 (JP)
- Takatori, Sunao c/o Yozan Inc.
 Setagay-ku, Tokyo 155 (JP)
- Sawahashi, Mamoru 1-59-17 Tomlokanishi, Yokohama-shi, Kanagawa 236, (JP)
- Adachi, Fumiyuki
 Yokohama-shi, Kanagawa 236 (JP)
- (74) Representative: Grünecker, Kinkeldey, Stockmair & Schwanhäusser Anwaltssozietät Maximilianstrasse 58 80538 München (DE)

(54) Matched filter circuit

(57) The present invention has an object to provide a matched filter circuit which is possible to synchronize a spreading code with an input signal. A matched filter according to the present invention samples input signal

in response to three clocks from the first to the third shifted by a half cycle of a sampling signal so as to judge whether the sampling clock is ahead or behind of the input signal according to signs of input signal sampled. One clock is selected to be the sampling clock.

Fig. 1





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